## **Array Design and Construction**





# **Probes**



<u>Clockwise from top:</u> 1: Side view of the assembled array showing its curvature and the holes in the back plate. 2: Front view of the array showing the height of the probe and the size of the array relative to a person. 3: View of the installed array in the LDX vacuum vessel showing the positioning and support pipe.



### **Electronics**





### <u>Amplifiers</u>

- Use the TI TLE2074 high-speed, low noise quad JFET opamp

- Configured for -3db knee at 480Khz and -0.01 gain
- low opamp bias current to minimize error 20 pA - high opamp input impedance – 1 T $\Omega$
- Schottkey Diodes referenced to power rails keep differential input less than the rails - small on-board input resistor protects against arcs side

